Form PTO-447A
(Rev. 7-98)

Staple to face of Application

U.S. DEPARTMENT OF COMMERCE
PATENT & TRADEMARK OFFICE

APPLICATION TRANSFER REQUEST FOR S.N. 09697305

Section I. TRANSFER REQUEST BY

Name **NGUYEN, VINH P** Date 2/7/2002

TO: Art Unit **2133** Class/sub **714/724+**

FROM: A.U. **2829** Class **324**

REASON:
test semiconductor using logic test

Gatekeeper concurrence *NTN*

Section IIa. DISPOSITION BY RECEIVING TC

By: *KD* A.U. *2109* Date *3/18/02*

NOT ACCEPTED ☐ Forward to receiving TC Post Classifier

REASON: *714 class definition requires either ① detecting/correcting errors in digital data or ② detecting/correcting faults in electrical computers, neither of which is herein claimed.*

Section IIb. DISPOSITION BY RECEIVING TC POST CLASSIFIER

☑ This dispute was resolved.   Forward to TC/AU *2133* Class/Sub *714/733+* Post Classifier *M. Fatehy* Date *3/25/02*

The claimed disclosure is directed to a built-in testing circuit Concurring _____
for generating test patterns for determining faults/errors in a semiconductor
☐ This dispute was not resolved, forward to DISPUTE RESOLUTION PANEL *circuit.*

Post Classifier Assessment:

Gatekeeper concurrence _____

Post Classifier _____ Date _____

Section III. DISPOSITION BY DISPUTE RESOLUTION PANEL

Date _____

Panel Decision: Forward To Technology Center/Art Unit _____ Class/sub _____ /_____

REASON:

Panel Member _____

Concurring Panel Member _____

3/20/02 Mahmoud Fatehiyan